

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Jeffrey S. Mailloux et al. Examiner: Hong Kim

Serial No.: 08/984,563 Group Art Unit: 2185

Filed: December 03, 1997 Docket: 303.623US4

For: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE
SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

RESPONSE TO NOTICE OF NON-COMPLIANT APPEAL BRIEF

Mail Stop Appeal Brief - Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This communication responds to the Notice of Non-Compliant Appeal Brief mailed on March 30, 2007. In compliance with 37 CFR § 1.121, the Appellant submits the following corrected section from Appellant's previously-submitted Appeal Brief, filed on January 25, 2007.

5. SUMMARY OF CLAIMED SUBJECT MATTER

This summary is presented in compliance with the requirements of Title 37 C.F.R. § 41.37(c)(1)(v), mandating a “concise explanation of the subject matter defined in each of the independent claims involved in the appeal ...”. Nothing contained in this summary is intended to change the specific language of the claims described, nor is the language of this summary to be construed so as to limit the scope of the claims in any way.

[Independent claim 59: Specification FIGs. 9, 12; and pg. 30, line 24 - pg. 33, line 21.]

Some embodiments of the invention are related to a method of accessing a memory, comprising receiving an external row address 154; choosing whether the memory is in a burst mode of operation or in a pipeline mode of operation 159; selecting a read operation or a write operation for the memory 150, 161; and executing a read or write operation in the chosen mode of operation.

[Independent claim 68: Specification FIGs. 9, 12; and pg. 30, line 24 - pg. 33, line 21.]

Some embodiments of the invention are related to a method for data transfer direction selection in a memory, comprising selecting a read or a write operation of the memory 150, 161; selecting a burst or a pipeline mode of operation for the memory 159; selecting an external address only data path 158; obtaining an external column address 151, 155; and accessing the memory when the pipeline mode of operation is selected 152, 156; and selecting an initial buffered external address data path 160; obtaining an initial external column address 162, 168; accessing the memory 165, 171; and generating internal column addresses when the burst mode of operation is selected 166, 172.

[Independent claim 69: Specification FIGs. 9-12; and pg. 25, line 7 - pg. 33, line 21.]

Some embodiments of the invention are related to a storage device 100 comprising mode circuitry 121 configured to select between a burst mode and a pipelined mode; selection circuitry 121 for selecting between a read operation and a write operation; an external column address data path 115 for pipeline read and write operation column address retrieval; an internal column

address generation module 149 for burst read and write operation column address generation; and pipelined/burst circuitry 138 coupled to the mode selection circuitry and configured to switch between the pipelined mode and the burst mode for operating the storage device in either mode.

This summary does not provide an exhaustive or exclusive view of the subject matter of the Application, and the Appellant refers the reader to the appended claims and their legal equivalents for a complete statement of the various claimed embodiments.

CONCLUSION

In accordance with 37 CFR 1.121, only the non-compliant sections of the Appellant's previously-submitted Appeal Brief has been included in this response.

The Appellant respectfully submits that the Examiner withdraw the non-compliant status and examine the Appeal Brief as appropriate.

The Examiner is invited to telephone the Appellant's attorney at (210) 308-5677 to facilitate prosecution of this Application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 11 day of April 2007.

Name

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